

Security and Privacy for the Internet of Things

Biplab Sikdar

Department of Electrical and Computer Engineering
National University of Singapore, Singapore

The Internet in 1969

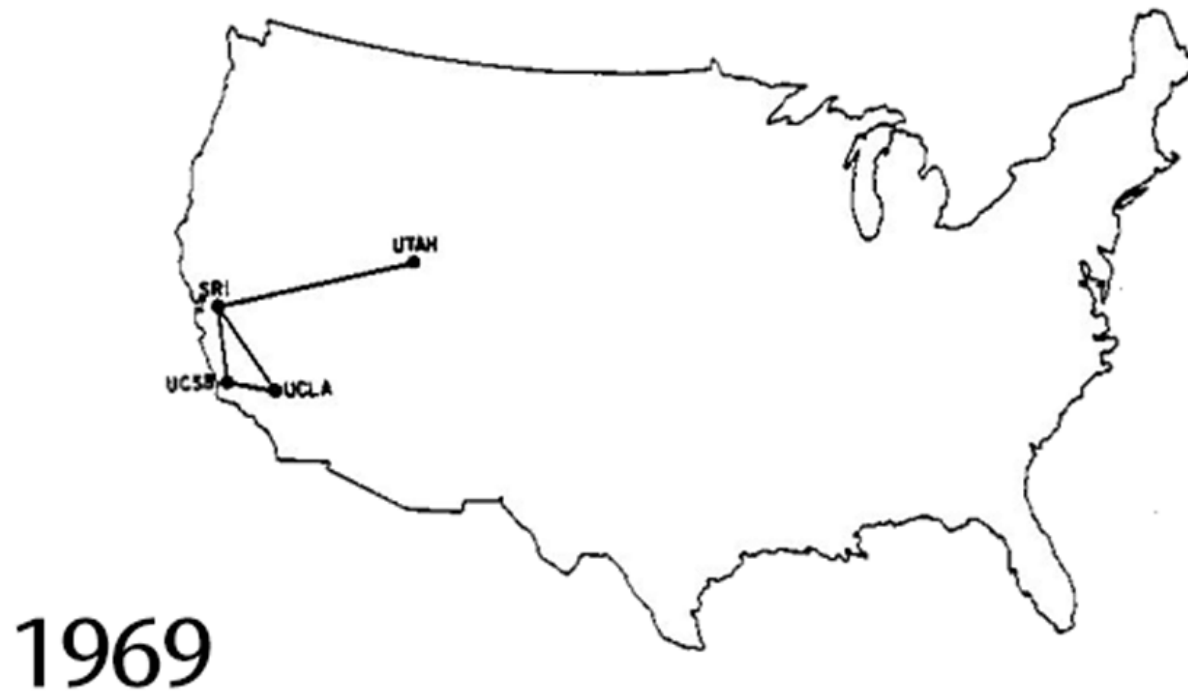
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- ❑ Four computers
- ❑ University of California, Los Angeles
- ❑ SRI (Stanford Research Institute)
- ❑ University of California, Santa Barbara
- ❑ University of Utah
- ❑ 29/10/1969: First packets sent. Charlie Kline attempted to remote login from UCLA to SRI. The system crashed on receiving “g”.



The Internet in 1969

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Common Applications Back Then

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- ❑ Telnet: Remote login
- ❑ Electronic mail (1971): 75% of network traffic in 1973
- ❑ File transfer protocol (1973)
- ❑ Network voice protocol (1977)
- ❑ Mailing lists (LISTSERVs): virtual discussion groups (one of the first was SF-LOVERS, dedicated to science fiction fans)

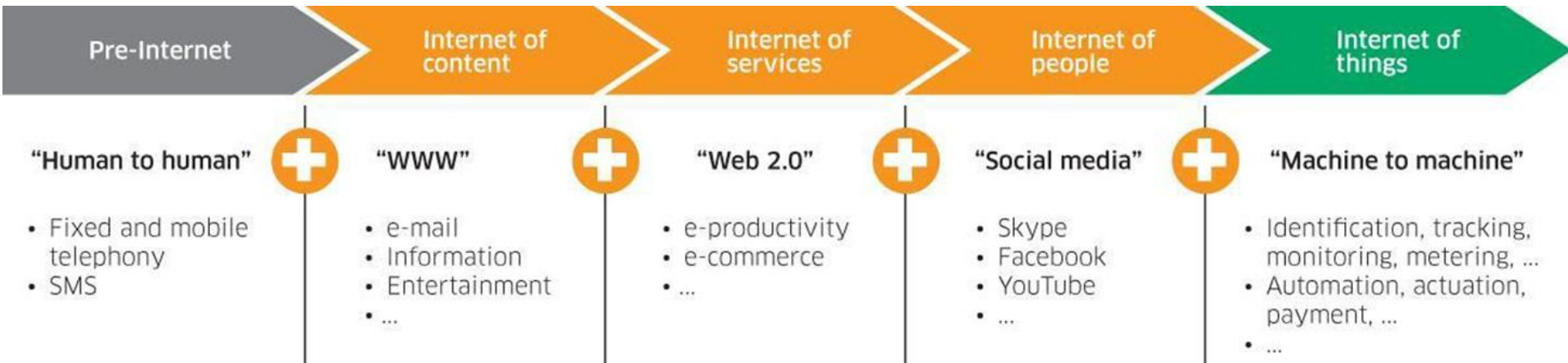
The Application that Changed it All

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- ❑ Hyper Text Transfer Protocol (HTTP): world wide web
- ❑ Led to the popularity of the “Internet”
- ❑ Internet commerce
- ❑ Social media
- ❑ Sharing economy

The Internet-of-Things: Evolution

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Source: Marc Jadoul, Nokia, "The IoT: The next step in internet evolution", 2015

IoT Application Domains

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IOT ANALYTICS

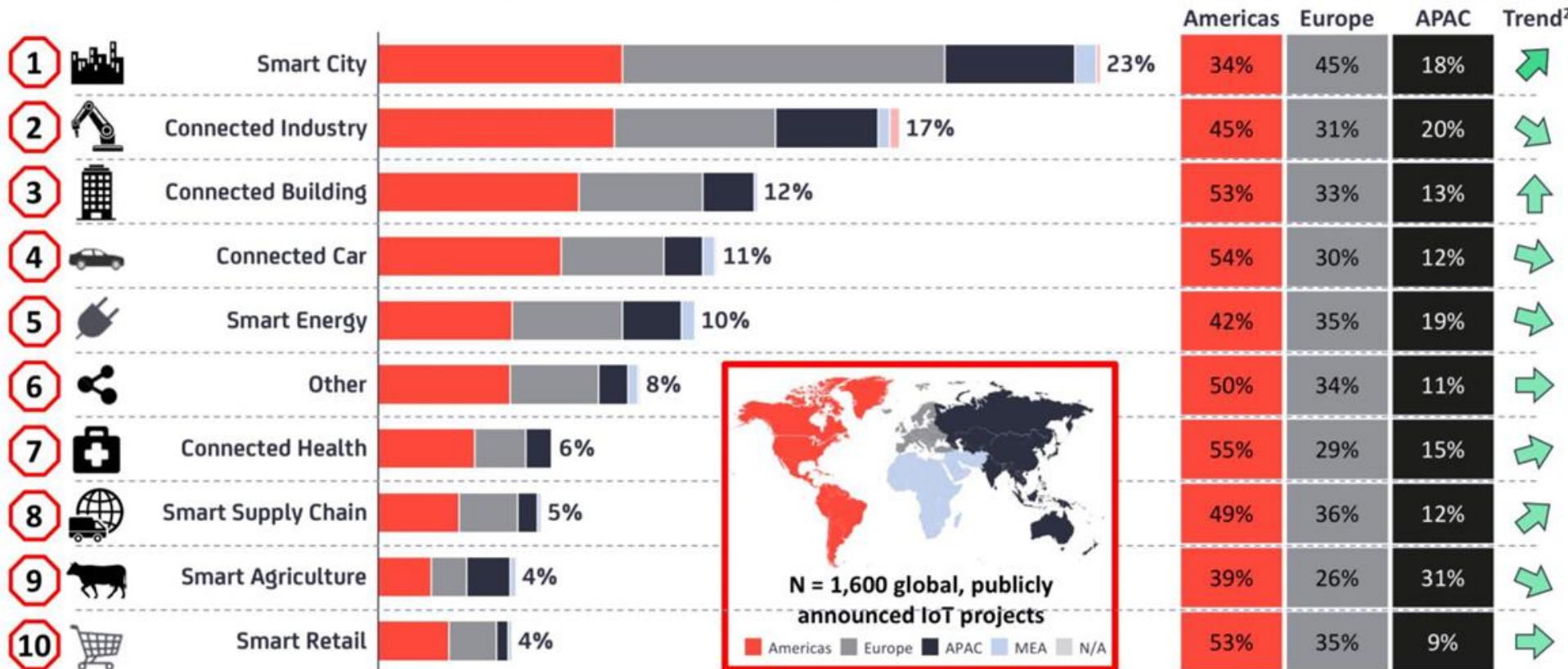
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Insights that empower you to understand IoT markets

IoT Segment

Global share of IoT projects¹

Details



The Internet-of-Things

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The Internet of Things (IoT) has a potential economic impact of 2.7-6.2 trillion USD until 2025

\$ trillion, annual



Who will capture this opportunity



Security Concerns

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Ukraine
Power
Outage

Stuxnet: Iran



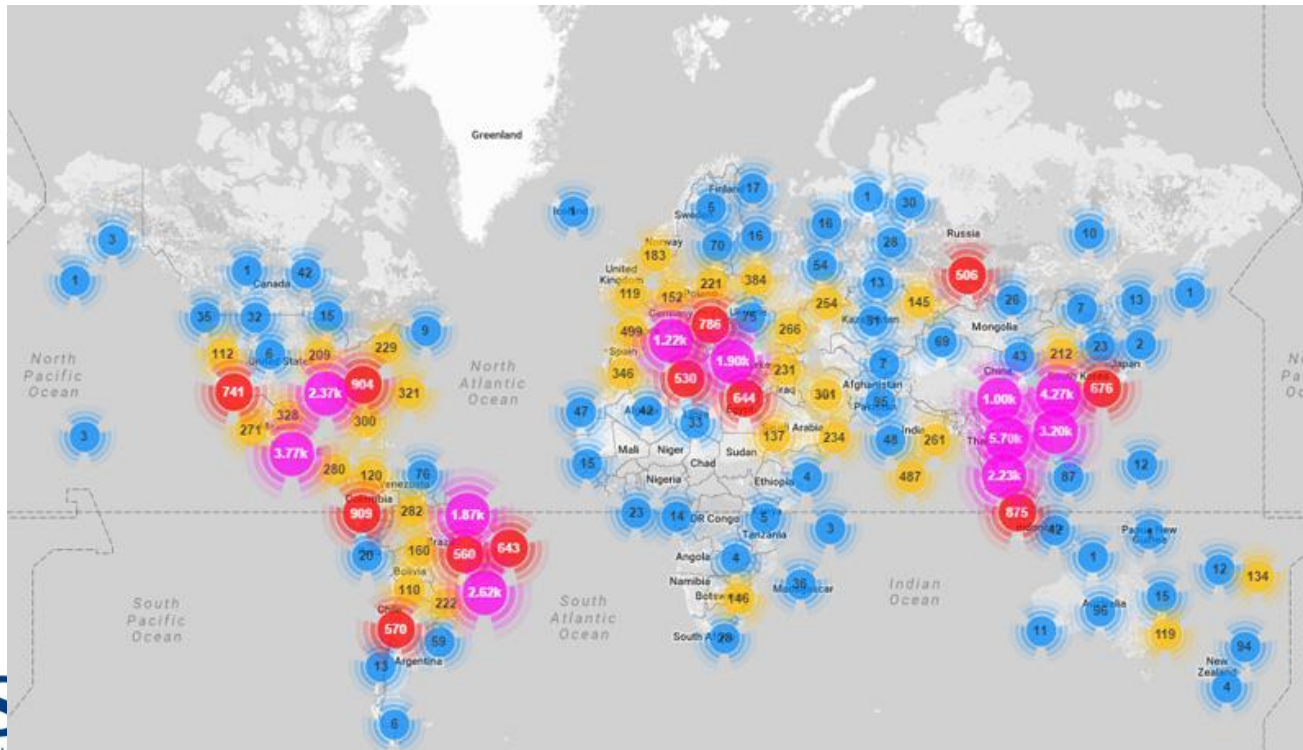
Lansing BWL
Ransomware



Saudi Aramco Cyberattack

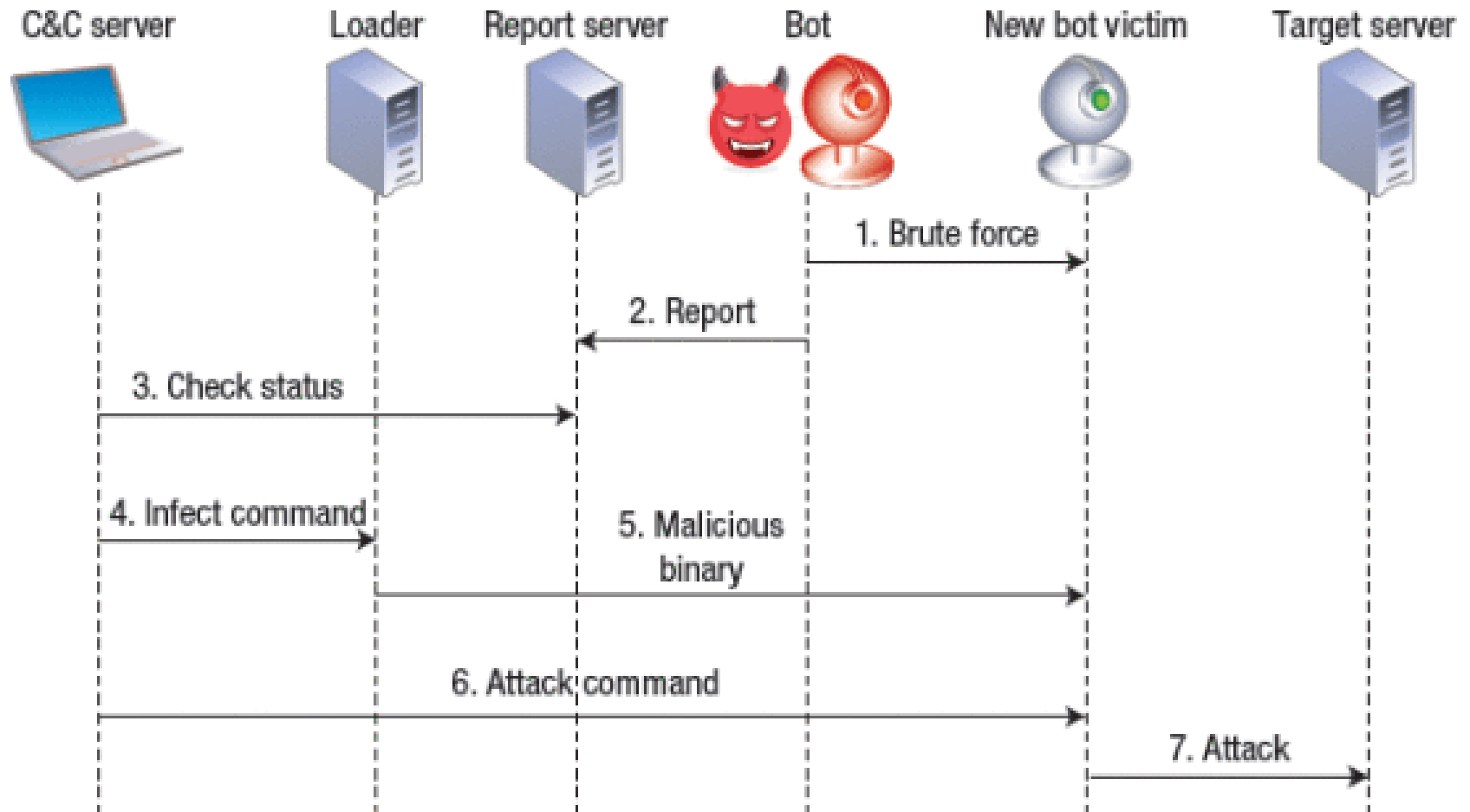
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Mirai Botnet Overview

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Mirai Botnet Attack

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- Took over a number of IoT Devices such as CCTV cameras, DVRs, routers
 - White-labeled DVR and IP cameras
 - username: root and password: xc3511
 - password hardcoded into device firmware



Other Attacks involving IoT Devices

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Hardware hacking



Samy Kamkar

Home Videos Playlists Channels Discussion About

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Part 1 HERE: Break open any Master Combo Lock in 8 tries or less using my online tool!

First Locked Position:
Second Locked Position:
Resistant Location:

Your Combination:
First Digit:

Part 2 HERE: Drilling open and explanation of cracking a combo lock using this method.



Combo Breaker - motorized combo lock crack
622,487 views 5 months ago
C-C-C-Combo Breaker is a motorized, battery powered device that can crack any Master combination lock!
<http://samy.pl/combobreaker/>

By Samy Kamkar

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Digital Hacking



“Junk hacking”



“Stunt hacking”



Security for the IoT

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- ❑ Authentication, Integrity, Confidentiality: application specific requirements
- ❑ Lightweight security protocols for constrained environments
- ❑ Privacy preserving service
- ❑ Trust and ownership issues
- ❑ Physical Security



Image: www.wordstream.com

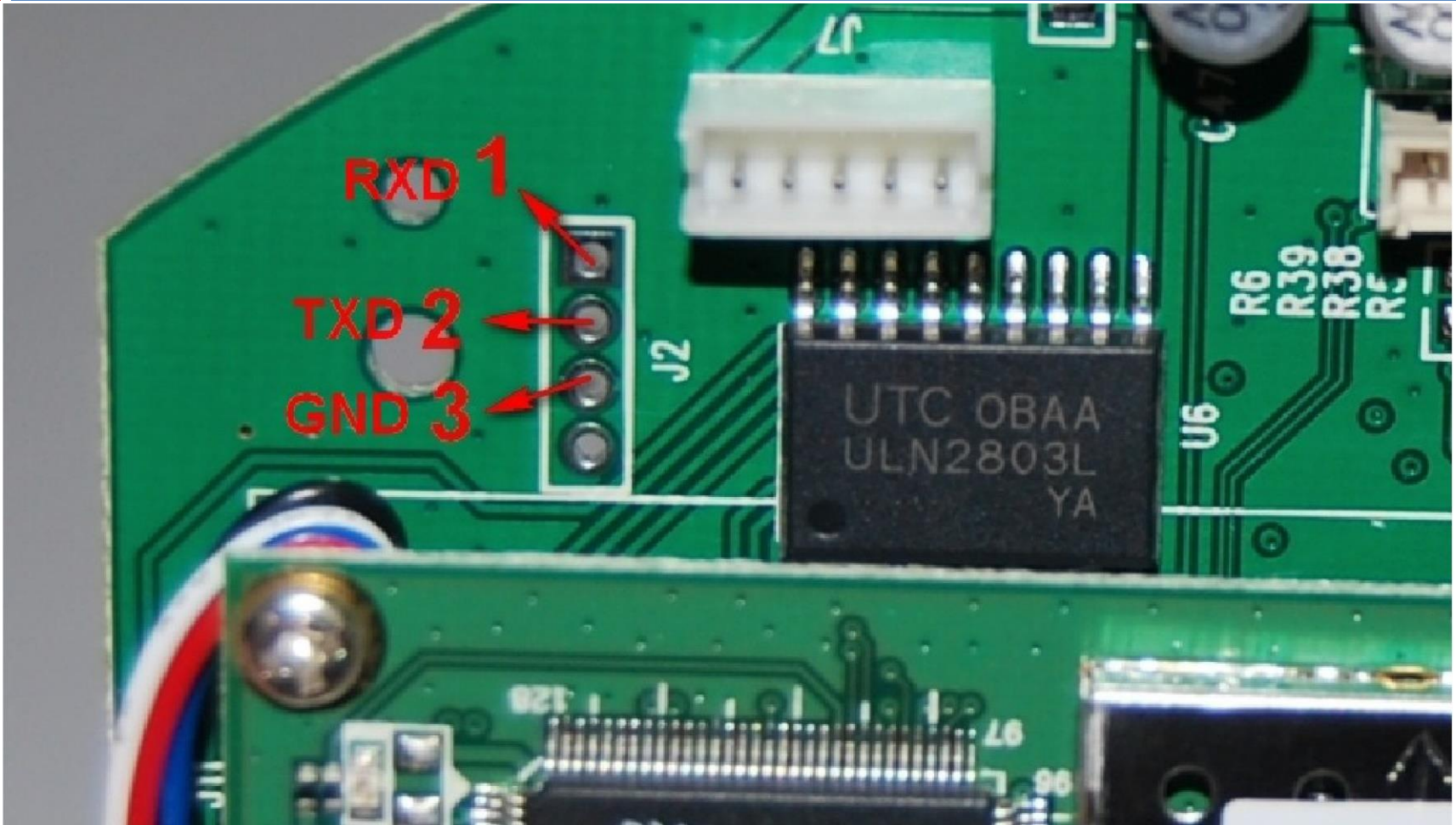
Why are IoT Devices Targeted?

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- ❑ Always on – IoT devices are rarely turned off
- ❑ Many manufacturers shy away from security in favor of usability
- ❑ IoT devices aren't checked on by users – “setup and forget”
- ❑ There are millions of them – this allows for a significant amount of DDoS traffic from these devices
- ❑ Users don't interact with their devices actively – less likely to notice a hijacker

Top IoT Vulnerabilities

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Security Challenges in IoT

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- ❑ Shared data with monetary value
- ❑ Attacks on end point devices can propagate quickly
- ❑ Large number of identical devices (homogeneity)
- ❑ No user Interface
- ❑ Applications may not tolerate errors, control critical equipment or processes
- ❑ Limited computing and battery power
- ❑ Limited visibility into or control over internal workings

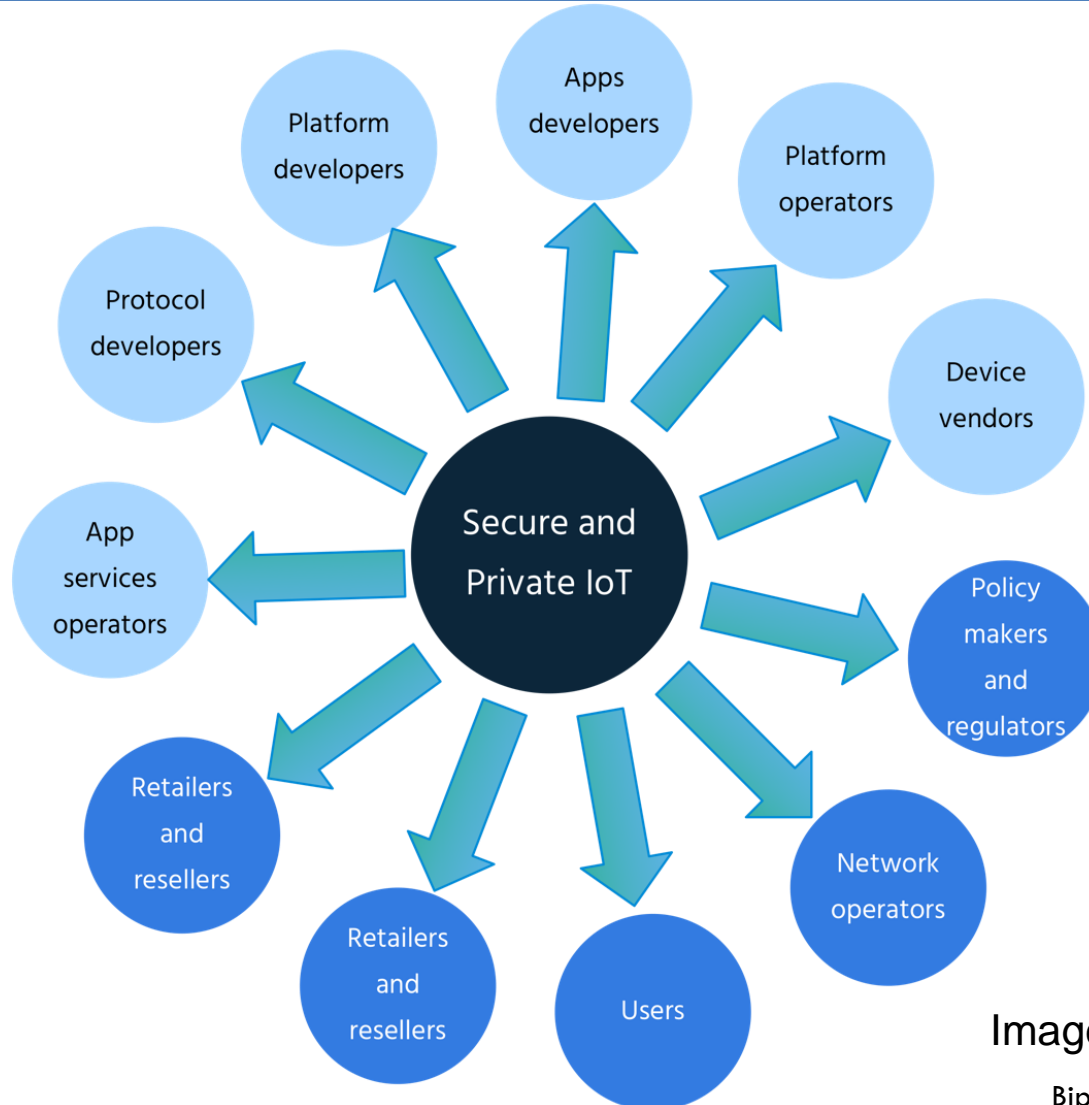
Securing an IoT System

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- ❑ Secure data handling framework: control over data and the sources and the consumers of data
- ❑ Establishment and trust management
- ❑ Access control and account management (for devices without UI)
- ❑ Use of secure protocols for data transmission
- ❑ Firewall management and antivirus updates
- ❑ Remote updates and patching for IoT devices

Securing an IoT System

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Physical Security for the IoT

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- ❑ Conventional approach: embed secure secrets in IC
 - ❑ Non-volatile memory (ROM, Fuse, Flash or EEPROM)
 - ❑ Battery-backed RAM
- ❑ Many IoT devices deployed in remote or unattended locations
- ❑ Small size of IoT devices: easy to conceal if stolen
- ❑ Attacks on a physically accessible device:
 - ❑ Opening the device to gain access to its component parts
 - ❑ Connecting a lead to access a physical port on the device
 - ❑ Contactless technology to detect device activity: electromagnetic radiation, high/low frequency sounds, power supply fluctuations

Solution: Hardware Security Primitives

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- ❑ Components that make up IoT devices include semiconductor based devices (ICs), passive components, sensors, batteries etc
- ❑ The manufacturing process of these components can provide them with unique characteristics
- ❑ Use these unique characteristics as security primitives or fingerprints

Physical Unclonable Functions

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- [Suh07] “A Physical Unclonable Function (PUF) is a function that maps a set of challenges to a set of responses based on an intractably complex physical system”
- Exploit process variations during IC fabrication
 - Variation is inherent in fabrication process
 - The variations are unique for each physical instance
 - The variations are hard to eliminate or predict
 - Relative variation tends to increase as the fabrication process moves to smaller sized components

Physical Unclonable Functions

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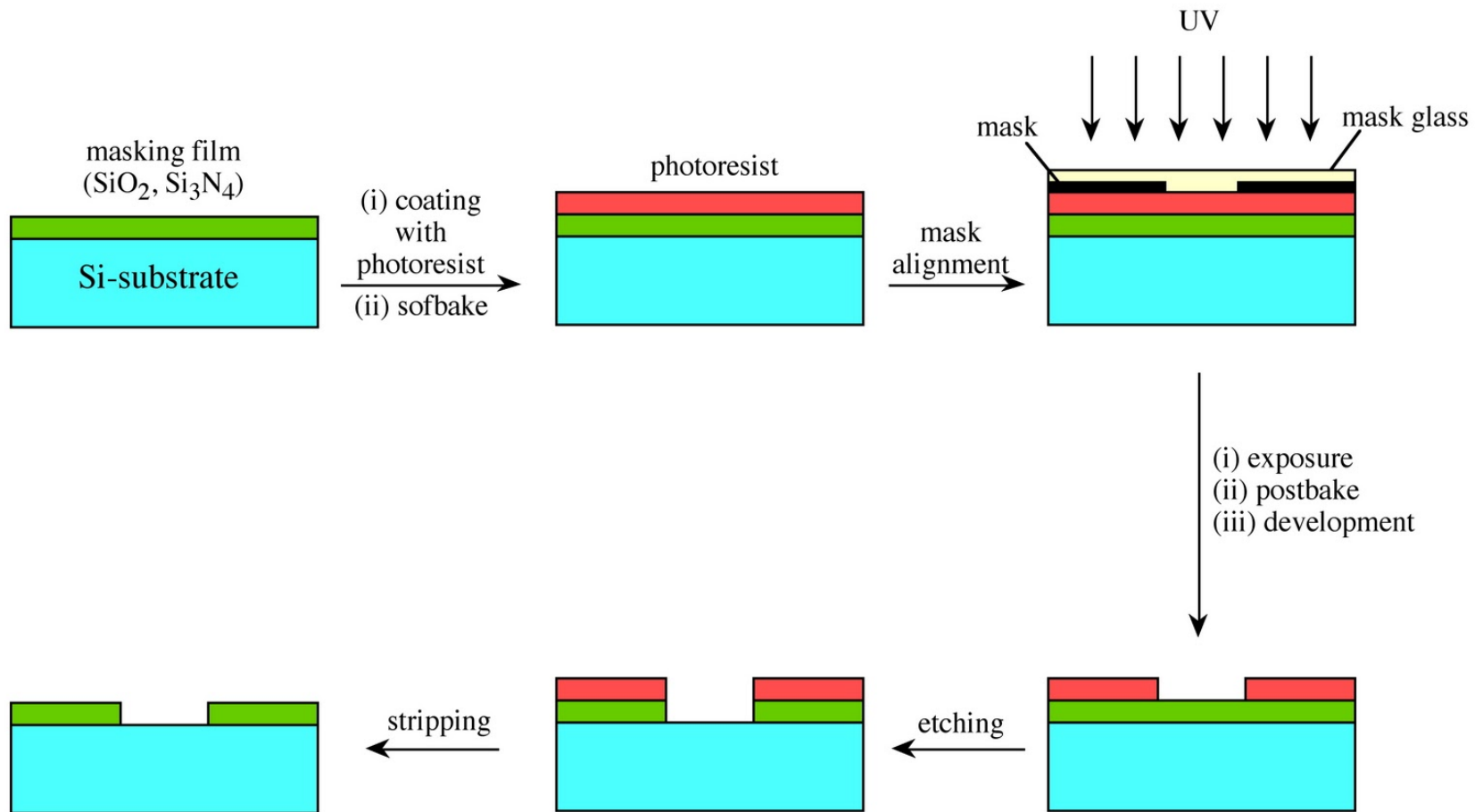
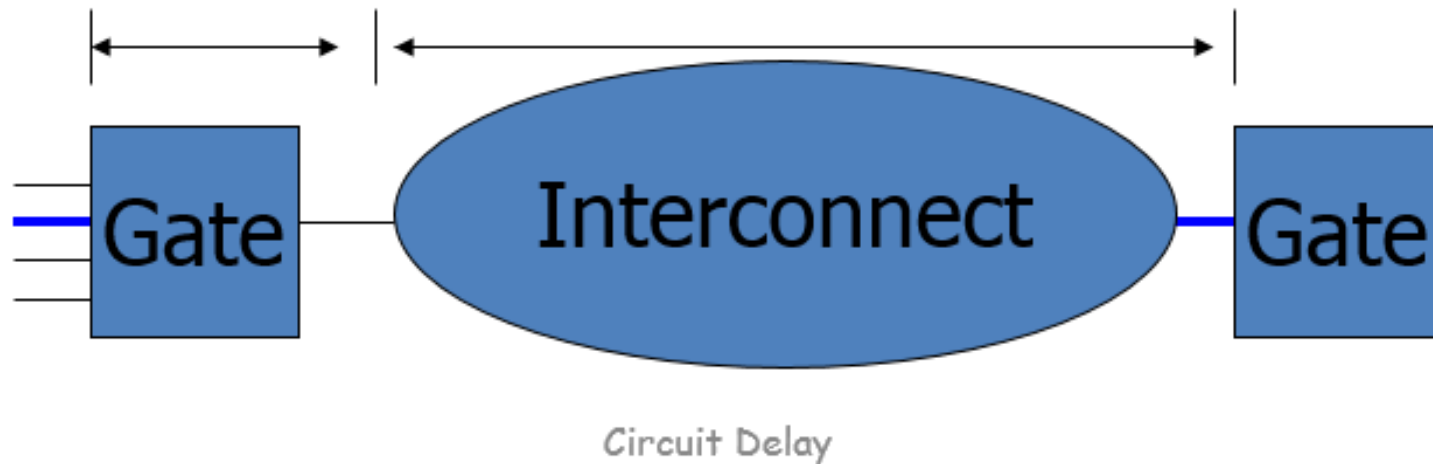


Image: Andrew Barron

Physical Unclonable Functions

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- Circuit delay = Interconnect delay + Gate delay



Physical Unclonable Functions

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- Designed versus fabricated features

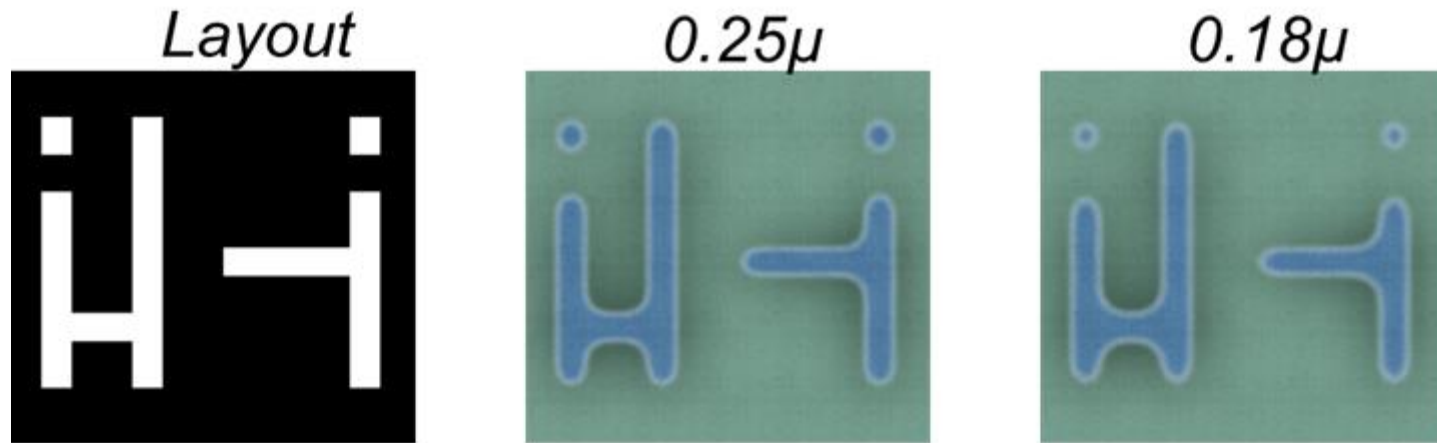


Image: Liu and Hu

Physical Unclonable Functions

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- Chip design cannot be reliably fabricated
 - Gap
 - Lithography technology: 193nm wavelength
 - VLSI technology: 45nm features

Technology node	130nm	90nm	65nm	45nm
Gate length (nm)	90	53	35	28
Tolerable variation (nm)	5.3	3.75	2.5	2
Wavelength (nm)	248	193	193	193

Source: Liu and Hu

Physical Unclonable Functions

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□ Chip

□ G

Large wavelength will degrade the printing quality, and thus there are significant variations on feature sizes (wire widths or channel wire). After printing, circuit delay can be significantly different from what it is designed.

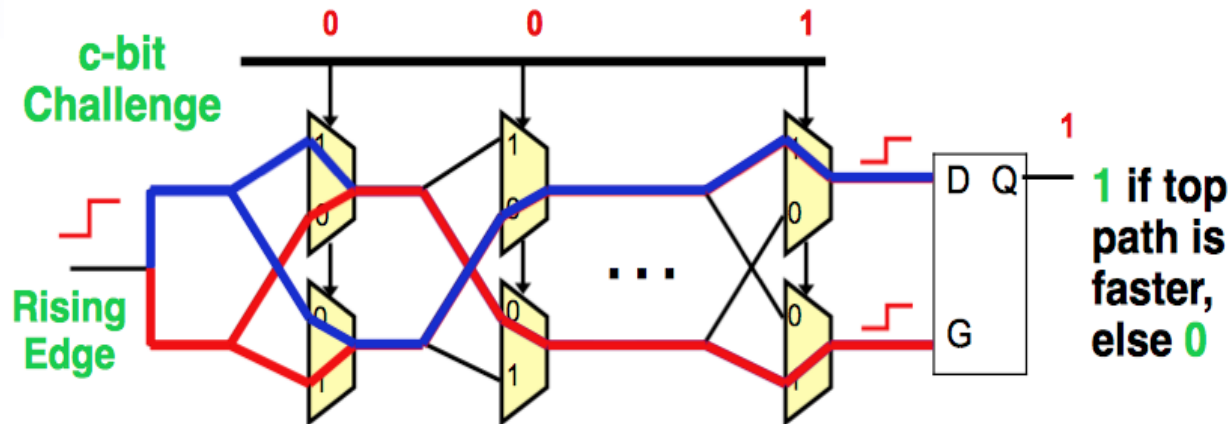
length

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Gate length (nm)	90	53	35	28
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Wavelength (nm)	248	193	193	193

Example: Arbiter PUF

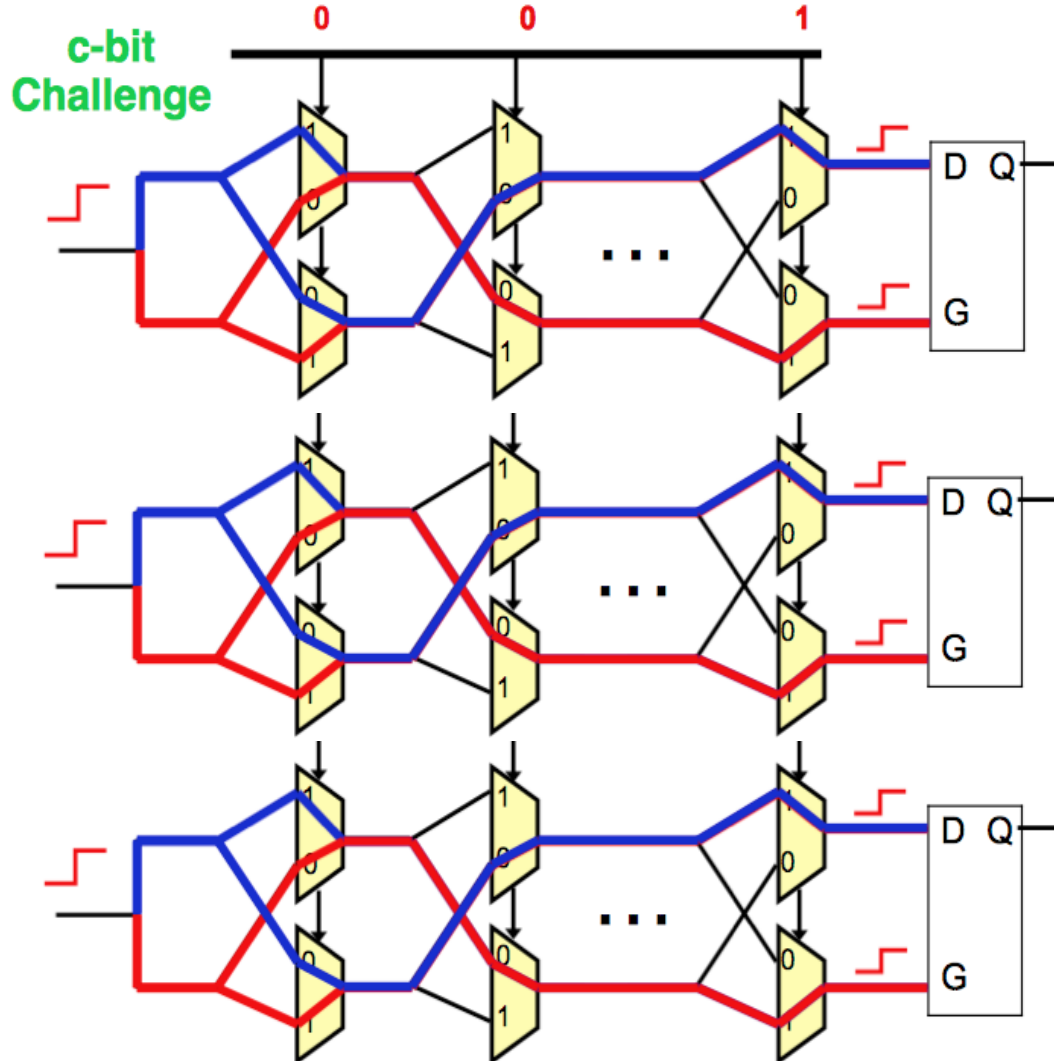
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- A c-bit challenge is given to the PUF
- Each challenge creates two paths through the circuit that are excited simultaneously
- The digital response is based on a (timing) comparison of the path delays



Example: Arbiter PUF

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Physical Unclonable Functions

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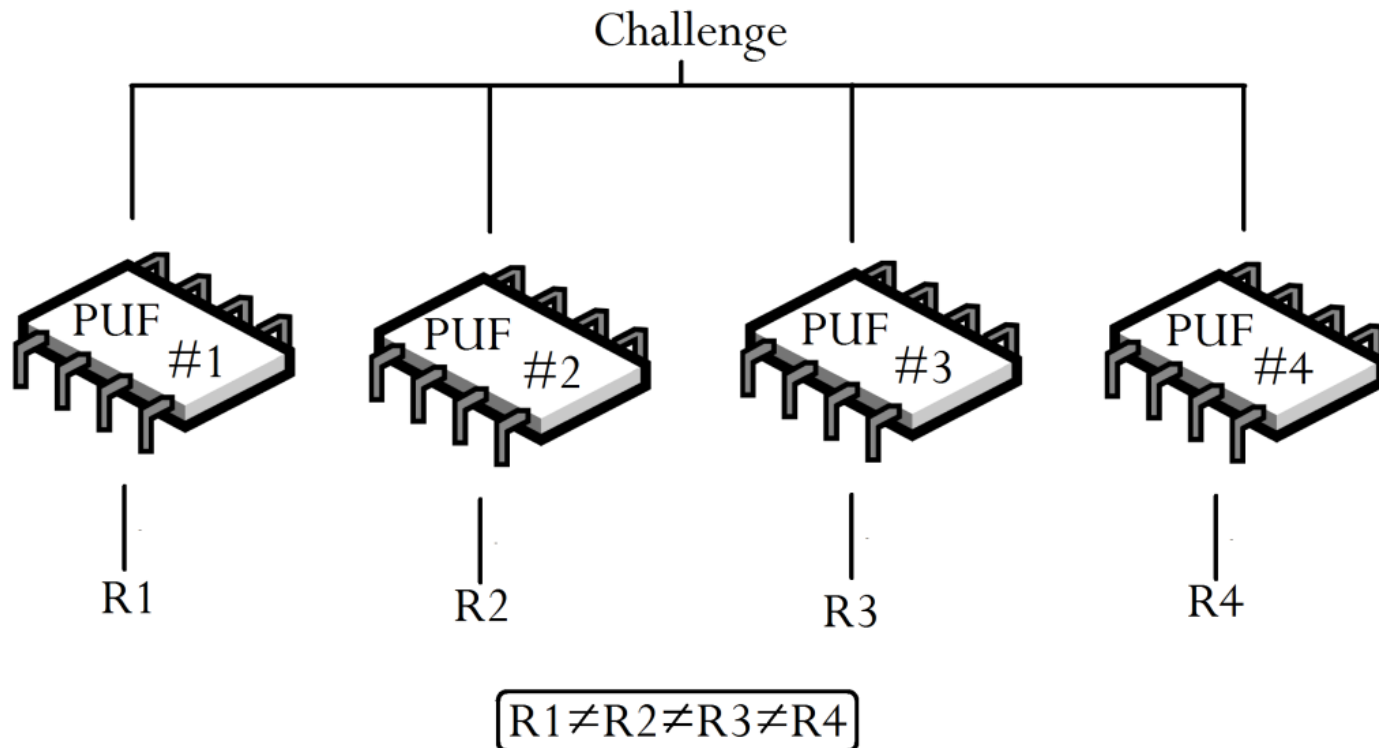


Image: Pedro Sosa

PUF Advantages

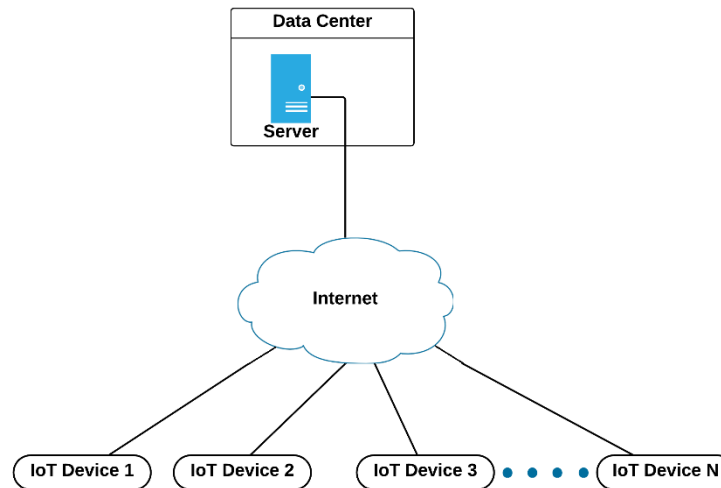
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- ❑ Higher physical security: secrets hidden in complex micro-structure of ICs and not non-volatile memory
- ❑ Side channel attacks:
 - ❑ Timing attacks: PUFs use CRPs instead of secret keys and accurately measuring the timing delays of a circuit in an IC is significantly more difficult.
 - ❑ Power monitoring attacks: designing the PUF such that the number of zeros and ones in the latches is constant
 - ❑ Electromagnetic attacks: reduce fluctuations in current
 - ❑ Differential fault analysis: physical data corruption inside cryptographic implementations to reveal internal state.

PUF Based Mutual Authentication

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□ Network model



□ PUF Assumptions

- Not possible to accurately model PUF
- Pair-wise PUF output-collision probability is zero
- Physical tampering will modify PUF

PUF Based Mutual Authentication

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□ Assumptions:

- The PUF and the device's microcontroller are considered to be on the same chip and inseparable.
- It is not possible to remove the PUF or tamper with the communication between the microcontroller and PUF.
- IoT devices are constrained by their resources, while the servers in the data center have no such limitation.
- IoT devices are physically unprotected and accessible by an adversary.
- An adversary can eavesdrop, modify, inject, and replay messages.

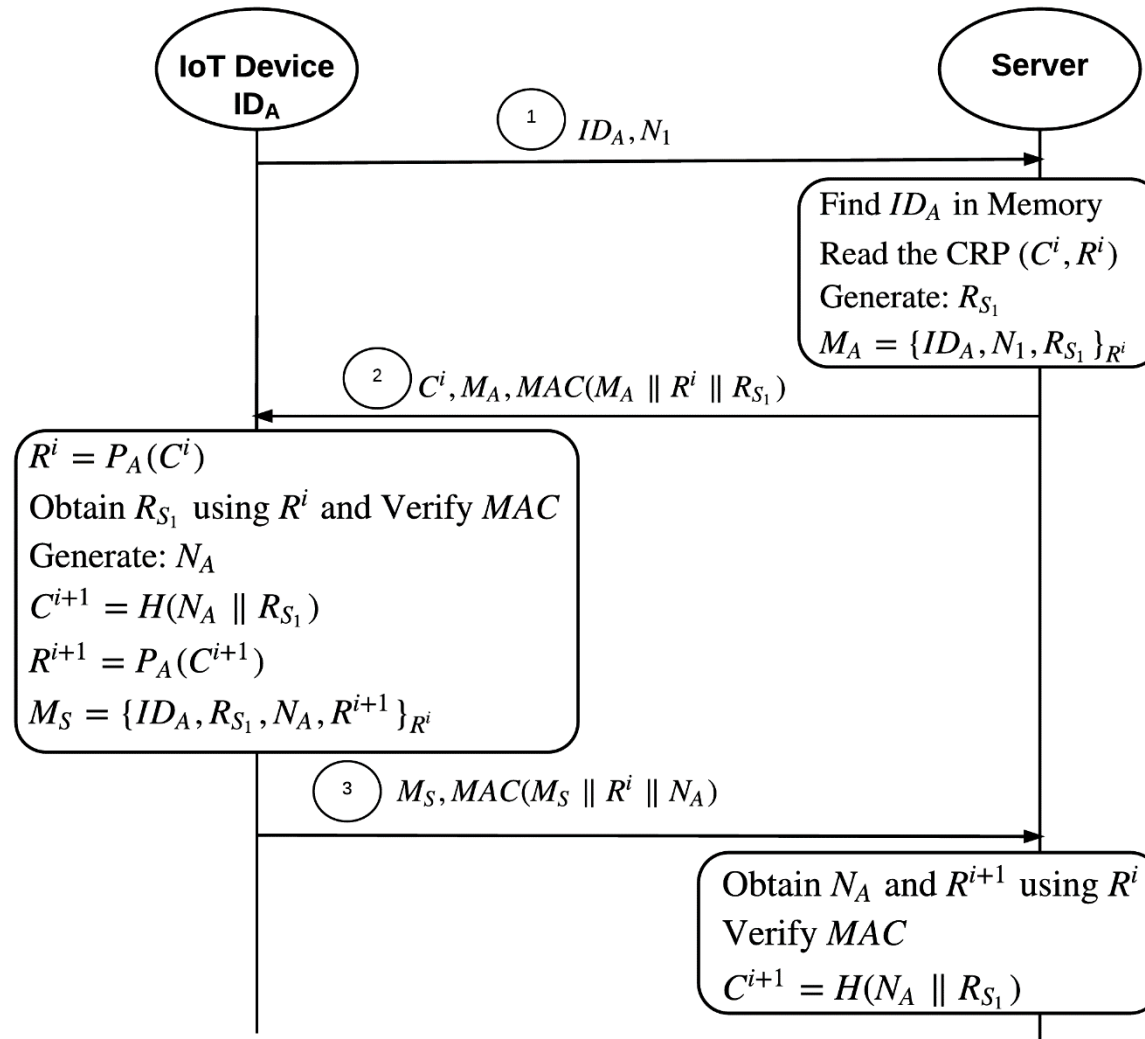
Notation

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Notation	Description
ID_i	ID of the IoT device
\oplus	XOR operation
$H(X)$	Hash of X
\parallel	Concatenation operator
$[Ex]_{Rec}$	Expression Ex is evaluated using the values from the received message
C^i	Challenge for the i 'th round
R^i	Response of the respective PUF for C^i

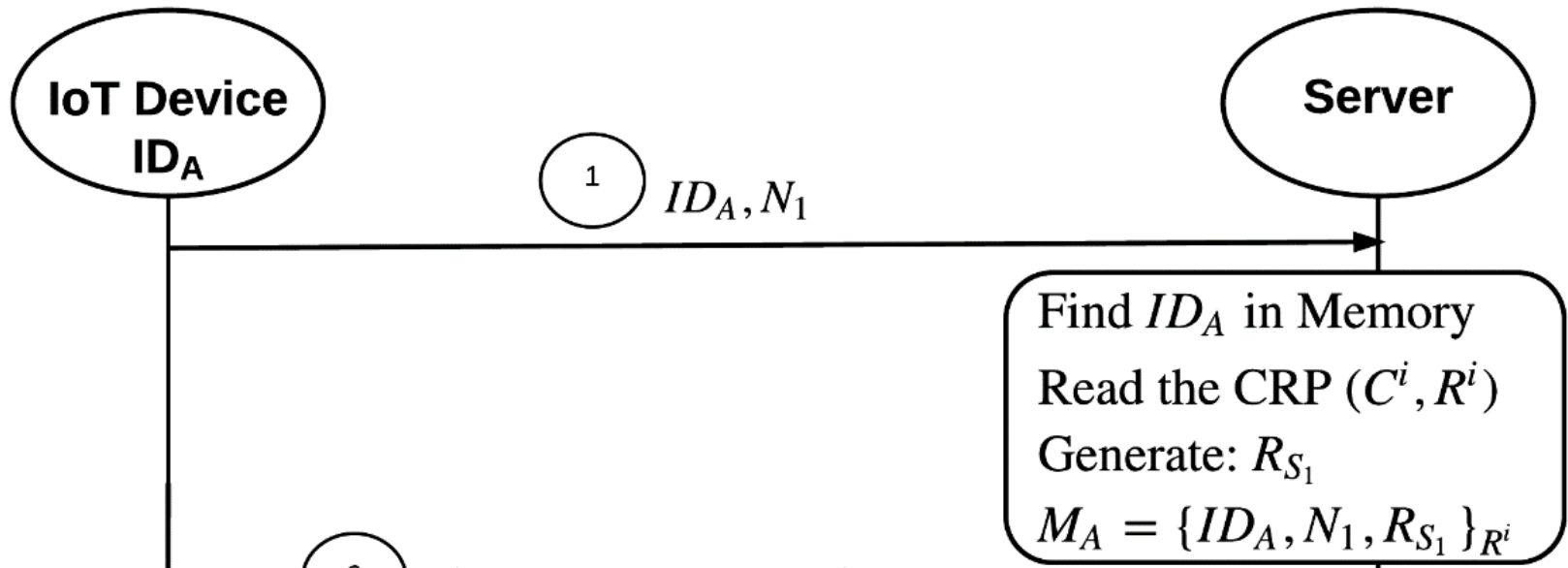
Authentication Protocol

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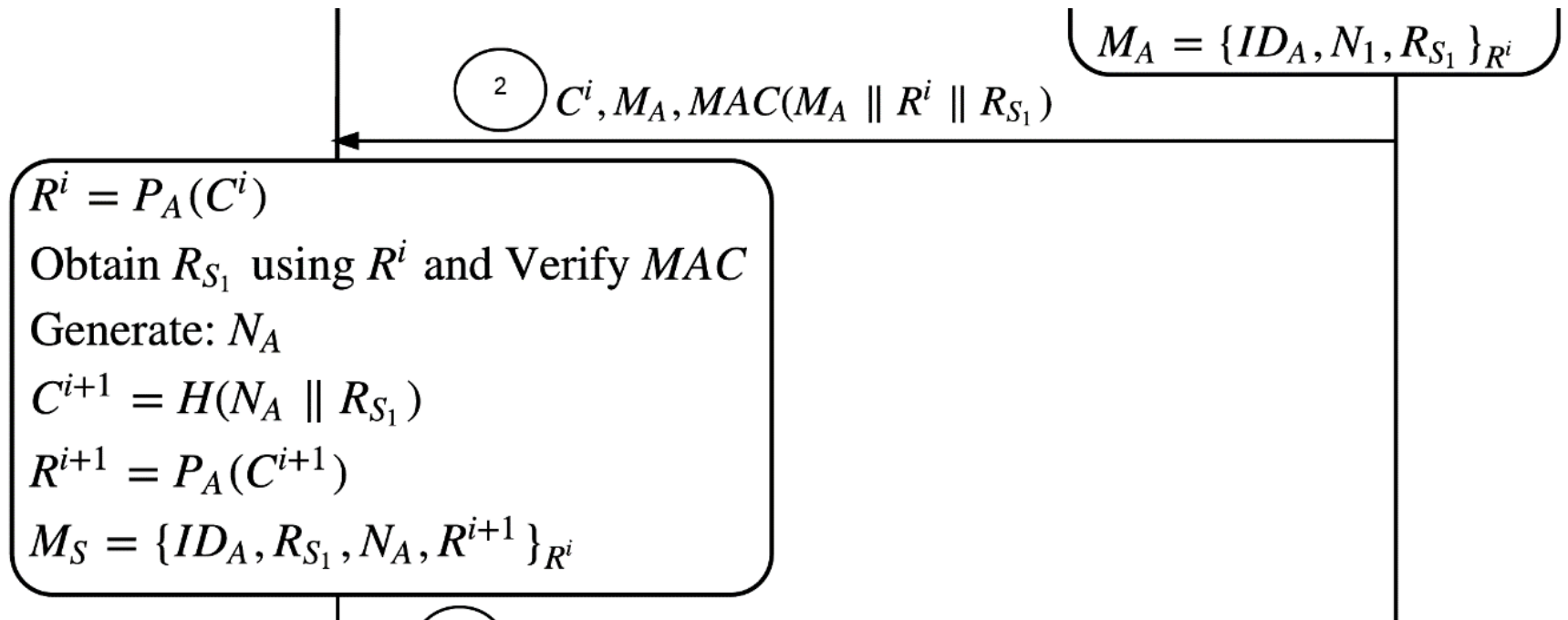
Authentication Protocol: Step 1

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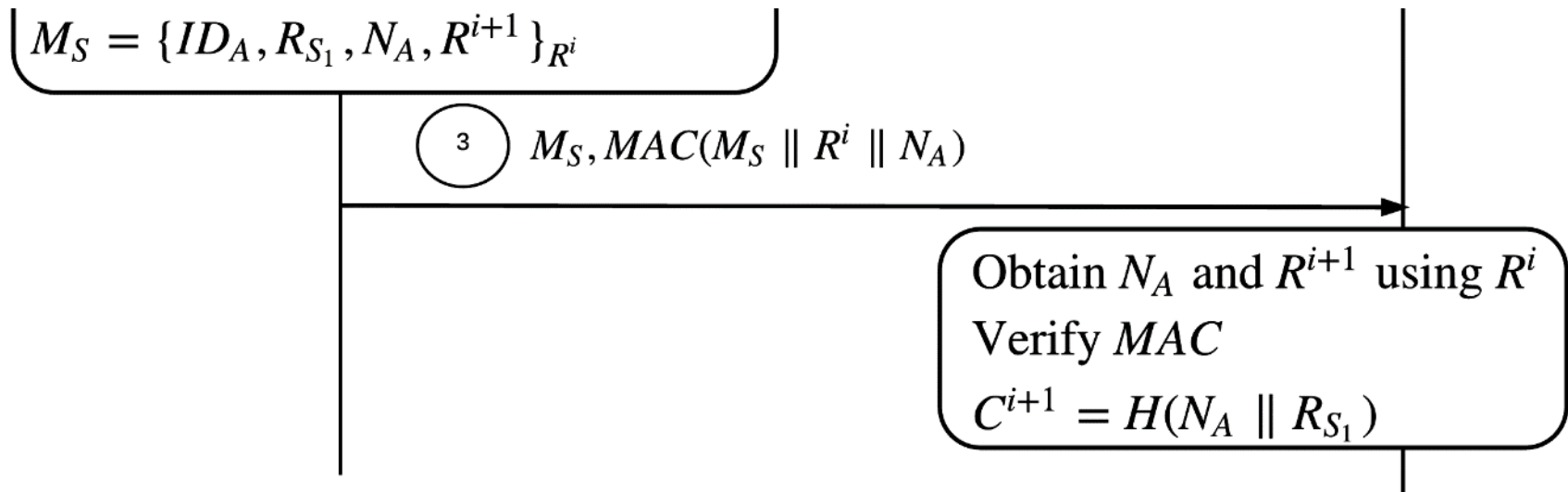
Authentication Protocol

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Authentication Protocol

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Proof of Correctness

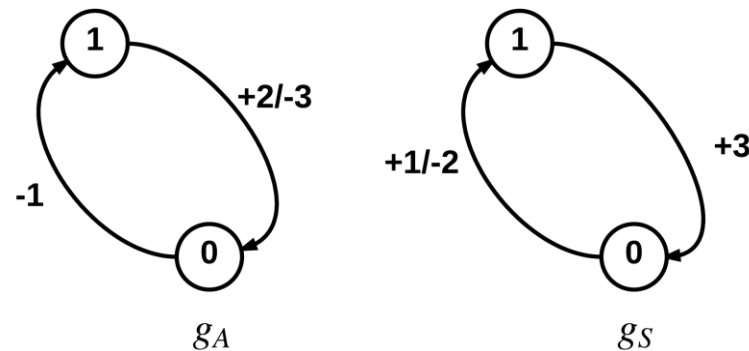
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- To prove correctness we need to show that the proposed protocols possess the following properties
 - **Completeness:** Protocol is able to accept all valid inputs
 - **Deadlock Freeness:** The protocol does not enter a state such that it stays in that state indefinitely.
 - **Livelock or Tempo-blocking freeness:** The protocol does not enter into an infinite loop.
 - **Termination:** When starting from the initial state, the protocol is always able to reach a well-defined final state.
 - **No non-executable interactions:** The protocol only contains transmission, reception, and interaction paths that are realized under normal operating conditions.

Proof of Correctness

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- Finite state machine for protocol entities



- $-m$ (respectively, $+m$) on the directed arcs represent a transmission (reception) of message m
- $+m/-n$ represents the reception of message m followed by the transmission of message n

Proof of Correctness

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- Reachability analysis:

$$\begin{array}{c}
 \begin{bmatrix} S_0 & E \\ E & S_0 \end{bmatrix}^{SS0} \\
 \downarrow A^{-1} \\
 \begin{bmatrix} S_1 & 1 \\ E & S_0 \end{bmatrix}^{SS1} \xleftarrow{S^{+1}} \begin{bmatrix} S_1 & E \\ 2 & S_1 \end{bmatrix}^{SS2} \xleftarrow{A^{+2}} \begin{bmatrix} S_0 & 3 \\ E & S_1 \end{bmatrix}^{SS4} \xleftarrow{S^{+3}} SS0
 \end{array}$$

- Potential deadlock state: not an initial or final state and does not have any messages in the channel
- The protocol does not have any potential deadlock states, implying deadlock freeness.

Verification

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- The logic has a set of inference rules
- Example:
$$\frac{P \mid \equiv Q \stackrel{k}{\leftrightarrow} P \wedge P \triangleleft \{X\}_k}{P \mid \equiv Q \mid \sim X}$$
 - message-meaning rule (if P believes P and Q share a key, then P ought to believe anything that it receives encrypted with the key comes from Q)

$$\begin{array}{c}
 \frac{A \models \#(N_1) \wedge \frac{A \models A \stackrel{R_i}{\leftrightarrow} S \wedge A \triangleleft^{R_i} N_1}{A \models S \stackrel{R_i}{\sim} N_1}}{A \models S \stackrel{R_i}{\leftrightarrow} S} \wedge A \models S \models \{S\}^c \triangleleft \parallel R_{S_1} \wedge \frac{A \models A \stackrel{R_i}{\leftrightarrow} S \wedge A \triangleleft^{R_i} R_{S_1}}{A \models S \stackrel{R_i}{\sim} R_{S_1}} \\
 \hline
 A \models S \models \{A, S\}^c \triangleleft \parallel R_{S_1} \wedge A \models \text{sup}(S) \wedge \frac{A \triangleleft^{R_i} N_1 \text{ R } R_{S_1}}{A \triangleleft N_1 \text{ R } R_{S_1}} \\
 \hline
 A \models \{A, S\}^c \triangleleft \parallel R_{S_1} \wedge A \models \text{sup}(S) \wedge A \models \#(R_{S_1}) \\
 \hline
 A \models A \stackrel{R_{S_1}}{\leftrightarrow} S
 \end{array}$$

Proof of “A believes R_{S_1} (N_A) is a good shared key of A and S”.

Verification

$$\frac{\frac{S \models A \overset{R^i}{\leftrightarrow} S \wedge S \models S^c \triangleleft \| R_{S_1} \wedge S \overset{R^i}{\sim} R_{S_1}}{S \models \{A, S\}^c \triangleleft \| R_{S_1}} \wedge A \models \#(R_{S_1})}{S \models A \overset{R_{S_1}}{\leftrightarrow} S}$$

Proof of “S believes R_{S_1} is a good shared key of A and S”.

$$\frac{\frac{A \models A \overset{R^i}{\leftrightarrow} S \wedge A \models S^c \triangleleft \| N_A \wedge A \overset{R^i}{\sim} N_A}{A \models \{A, S\}^c \triangleleft \| N_A} \wedge A \models \#(N_A)}{A \models A \overset{N_A}{\leftrightarrow} S}$$

Proof of “A believes N_A N_B is a good shared key of A and S”.

Verification

$$\begin{array}{c}
 \frac{S \models \#(R_{S_1}) \wedge \frac{S \models A \overset{R^i}{\leftrightarrow} S \wedge S \overset{R^i}{\triangleleft} R_{S_1}}{S \models A \overset{R^i}{\sim} R_{S_1}}}{S \models A \overset{R^i}{\leftrightarrow} S} \wedge \frac{S \models A \models \{S\}^c \triangleleft N_A \wedge \frac{S \models A \overset{R^i}{\leftrightarrow} S \wedge S \overset{R^i}{\triangleleft} N_A}{S \models A \overset{R^i}{\sim} N_A}}{S \models \{A, S\}^c \triangleleft N_A} \wedge \frac{S \models \#(R_{S_1}) \wedge \frac{A \overset{R^i}{\triangleleft} R_{S_1} \text{ R } N_A}{S \triangleleft R_{S_1} \text{ R } N_A}}{S \models \#(N_A)} \\
 \hline
 S \models A \overset{N_A}{\leftrightarrow} S
 \end{array}$$

Proof of “S believes N_A is a good shared key of A and S”.

$$\begin{array}{c}
 \frac{A \models A \overset{R^i}{\leftrightarrow} S \wedge A \models S^c \triangleleft R^{i+1} \wedge A \overset{R^i}{\sim} R^{i+1}}{A \models \{A, S\}^c \triangleleft R^{i+1}} \wedge A \models \#(R^{i+1}) \\
 \hline
 A \models A \overset{R^{i+1}}{\leftrightarrow} S
 \end{array}$$

Proof of “A believes R_{i+1} is a good shared key of A and S”

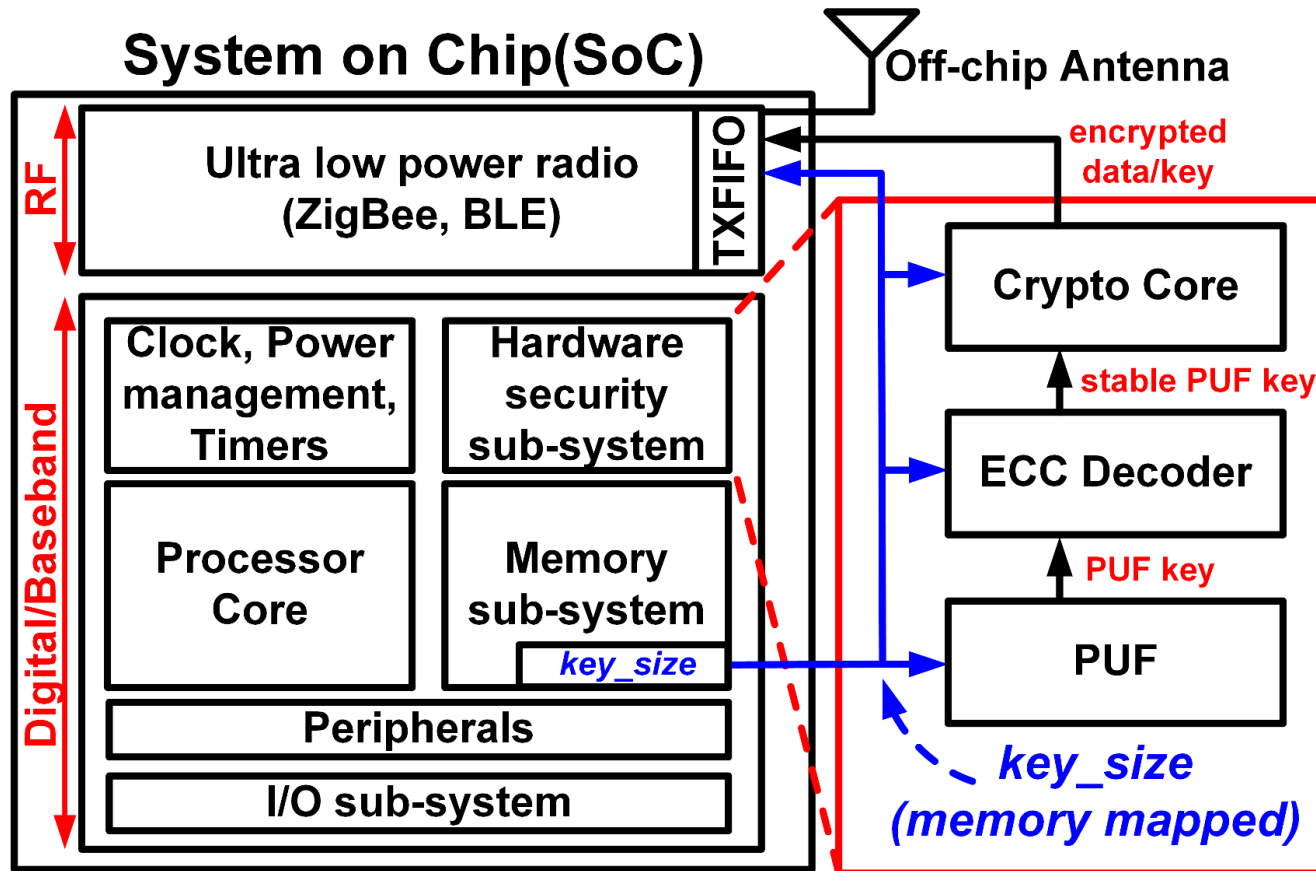
Verification

$$\begin{array}{c}
 \frac{S \models \#(R_{S_1}) \wedge \frac{S \models A \overset{R^i}{\leftrightarrow} S \wedge S \triangleleft R_{S_1}}{S \models A \overset{R^i}{\sim} R_{S_1}}}{S \models A \overset{R^i}{\leftrightarrow} S} \wedge S \models A \models \{S\}^c \triangleleft R^{i+1} \wedge \frac{S \models A \overset{R^i}{\leftrightarrow} S \wedge S \triangleleft R^{i+1}}{S \models A \overset{R^i}{\sim} R^{i+1}} \wedge \frac{S \models \#(R_{S_1}) \wedge \frac{A \triangleleft R_{S_1} \quad \mathbf{R} \quad R^{i+1}}{S \triangleleft R_{S_1} \quad \mathbf{R} \quad R^{i+1}}}{S \models \#(R^{i+1})} \\
 \hline
 S \models \{A, S\}^c \triangleleft R^{i+1} \wedge S \models \#(R^{i+1}) \\
 \hline
 S \models A \overset{R^{i+1}}{\leftrightarrow} S
 \end{array}$$

Proof of “S believes R_{i+1} is a good shared key of A and S”

Implementation

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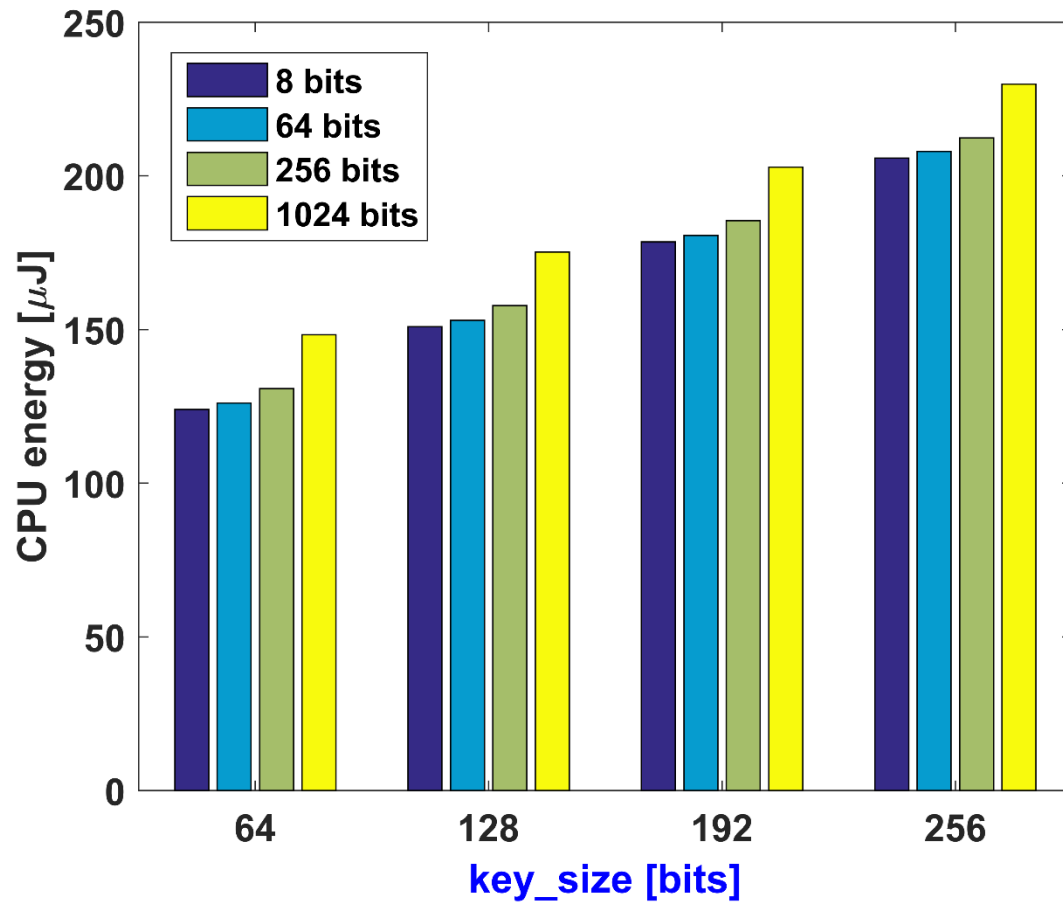
Energy Cost of Building Blocks

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Sub-component	Domain	Energy	Example
PUF	Baseband (digital)	10 - 200 fJ/bit	PUF [14], [15]
ECC	Baseband (digital)	20 - 60 pJ/bit	BCH [27], [28]
Crypto	Baseband (digital)	1 - 30 pJ/bit	AES [24] - [26]
Wireless	Radio Frequency (RF)	2-10 nJ/bit	BLE [1]

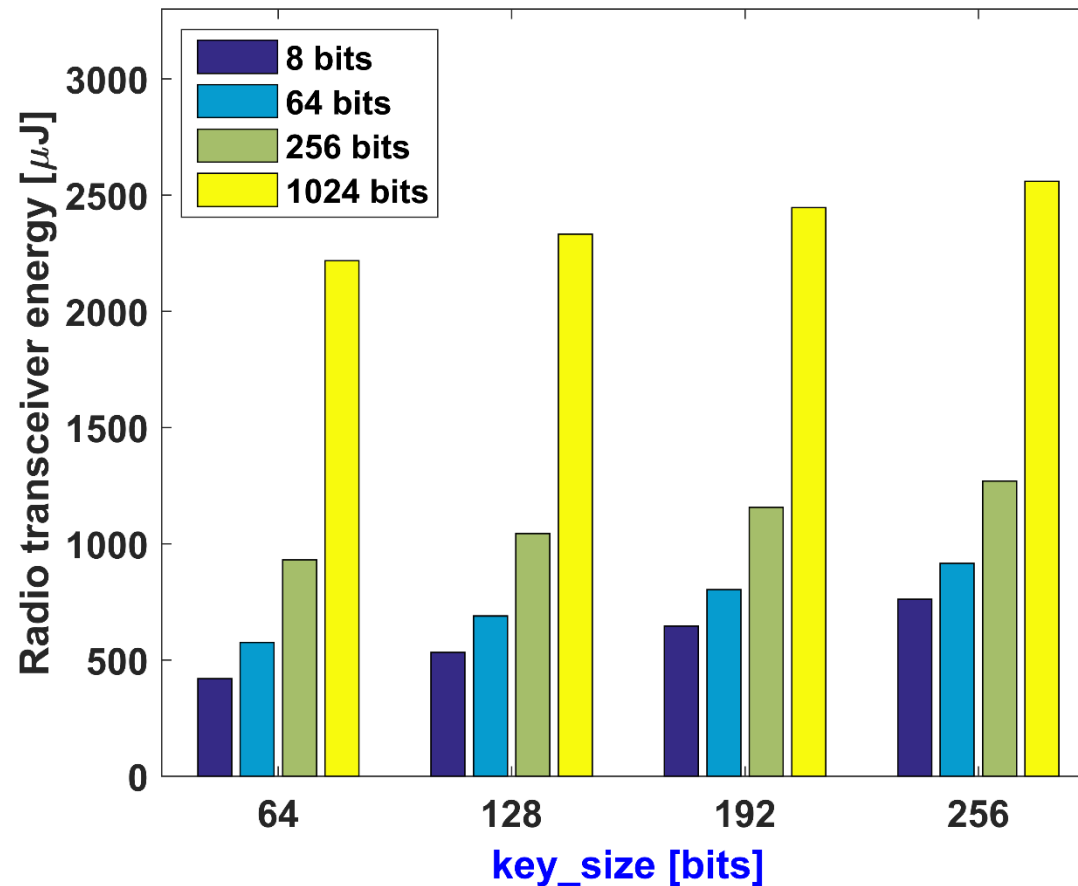
Energy Consumption

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Energy Consumption

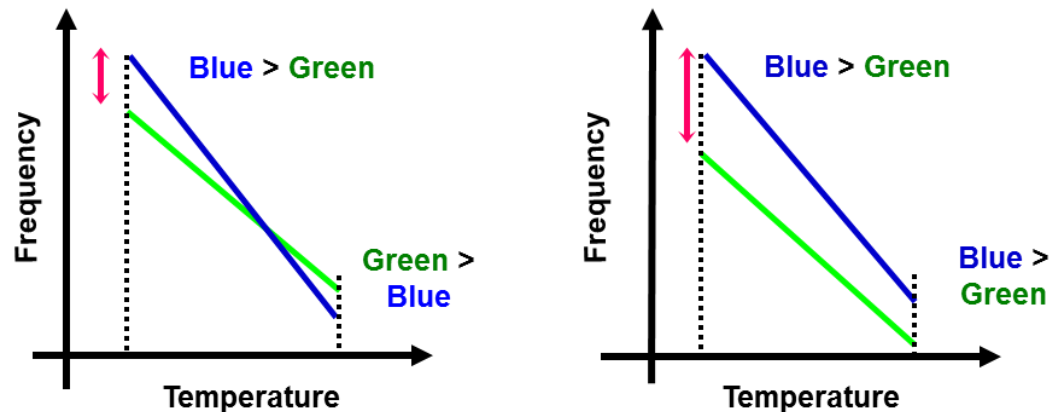
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PUF Issues

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- PUF output bit may “flip” when environmental conditions change (e.g. ring oscillator PUF [Tri07])



- Machine learning attacks on PUFs

Conclusions

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- ❑ IoT presents a number of security challenges
- ❑ Coordinated efforts are required at all layers and by all stakeholders
- ❑ There are many promising solutions: PUFs

Thank You

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